

## ABSTRACT OF THE DISCLOSURE

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An IC card has a card substrate with semiconductor integrated circuit chips mounted thereon and a plurality of connector terminals formed thereon. The connector terminals are exposed from a casing and are laid out in staggered sequences adjacent to one another forward and backward as viewed in an IC card inserting direction, whereby a structure or configuration wherein the amounts of protrusions of socket terminals of a card socket are changed and the socket terminals are laid out in tandem, can be adopted with relative ease. If a connector terminal arrangement of a low-order IC card is adopted as a specific connector terminal sequence, and a function dedicated for a high-order IC card is assigned to another staggered connector terminal arrangement, then backward compatibility can also be implemented with ease.